

EAST - [Text_Search.wsp:1]

File View Edit Tools Window Help

Pending

Active

- L1: (292) (discharge adj (lamp or lighting)) and
- L2: (82) 1 and inverter and (square adj wave)
- L3: (80) 2 and frequency
- L4: (62) 3 and resona\$4 and inductor and capac
- L5: (62) 4 and control\$4
- L6: (62) 5 and switch\$3
- L7: (59) 6 and winding
- L8: (4) 7 and microprocessor
- L9: (36) 7 and (PWM or (pulse\$1 adj width adj r
- L10: (35) 9 and compar\$4
- L11: (35) 10 and transformer

Failed

Saved

Favorites

Tagged (0)

UDC

Queue

Trash

Search

Lib

Browse

Query

Clear

DBs

USPAT; US-PGPUB; EPO; JPO; DERWENT

Plurals

Highlight all hit terms initially

Default operator: OR

BRS form

ISAR form

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	C	E	Err
1	BRS	L1	292	(discharge adj (lamp or lighting)) and (AC or (alternating adj current)) and (DC or (direct adj current)) and conver\$4 and (power near2 regulat\$3)	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 09:53			0
2	BRS	L2	82	1 and inverter and (square adj wave)	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 09:53			0
3	BRS	L3	80	2 and frequency	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 09:53			0
4	BRS	L4	62	3 and resona\$4 and inductor and capacitor	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 09:54			0
5	BRS	L5	62	4 and control\$4	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 10:37			0
6	BRS	L6	62	5 and switch\$3	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 11:04			0
7	BRS	L7	59	6 and winding	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 11:04			0
8	BRS	L8	4	7 and microprocessor	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 11:05			0
9	BRS	L9	36	7 and (PWM or (pulse\$1 adj width adj modulat\$3))	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 11:06			0
10	BRS	L10	35	9 and compar\$4	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 11:07			0
11	BRS	L11	35	10 and transformer	USPAT, US-PGPUB; EPO; JPO; DERWENT	2004/09/02 11:07			0